

#4A/1-13-04  
v Jones

**PATENT**  
Attorney Docket No. 401191

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of:

HATAKENAKA et al.

Application No.: Unassigned

Art Unit: Unassigned

Filed: June 4, 2001

Examiner: Unassigned

For: SEMICONDUCTOR  
INTEGRATED CIRCUIT  
DEVICE COMPRISING  
SYNCHRONOUS DRAM  
CORE AND LOGIC  
CIRCUIT INTEGRATED  
INTO A SINGLE CHIP  
AND METHOD OF  
TESTING THE  
SYNCHRONOUS DRAM  
CORE

**PRELIMINARY AMENDMENT**

Commissioner for Patents  
Washington, D. C. 20231

Dear Sir:

Prior to the examination of the above-identified patent application, please enter the following amendments and consider the following remarks.